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In fact, we see more and more application-specific SoCs for mobile applications, audioand video-based multimedia products ... Scalable multistage networks for multiprocessor system-on-chip design. In ...

MpNoC Design: Modeling and Simulation

Monitored systems ... reference design that supports the i.MX31. Freescale's MPC5121e is a 32-bit system-on-chip that combines Power Architecture technology with 3-D graphics and multimedia ...

Telematics catches a WAVE

e-MDT to offer turnkey SH-4 based SoC design ... and will offer a multiprocessor-based system, based on the SH4-501S. "The industry standard SuperH family perfectly complements our IP library and ...

Korean design company e-MDT licenses SuperH 32-bit SH-4 CPU core His MTech thesis - "A Multimedia Data Management System with Text ... In order to position India as a global hub for Electronics System Design and Manufacturing (ESDM), MeitY has designed and ...

MeitY Gets 'Mightier': India's IT Ministry Gets A Reboot With 2 Accomplished Technocrat-Entrepreneurs At The Helm We will admit it: mostly when we see a homebrew CPU design on an FPGA, it is a simple design that wouldn't raise any eyebrows in the 1970s or 1980s. Not so with [Henry Wong's] design ...

FPGA Soft CPU Is Superscalar

The SHF program seeks proposals addressing foundational computer science and engineering research and education to enable and advance the design, analysis, use and evolution of software and hardware.

CISE - CCF

This means that companies will be able to "customize features for cameras, displays, graphics, artificial intelligence (AI) processing units (APUs), sensors and connectivity sub-systems within t ...

MediaTek announces Dimensity 5G Open Resource Architecture The Data Science Laboratory is devoted to the extraction of knowledge from data and to the theory, design, and implementation of information systems to manage ... and genomics and proteomics data. The ...

Department of Computer Science and Engineering

The recent component shortage has highlighted the fragility of current supply chains, asserted Siemens, as it announced the acquisition of electronics Design-to-Source platform ... Electronic Board ...

Programmable Logic and Asic CR-5000 offers highly sophisticated functionality for the design of multi-layer and high-speed boards, addressing design challenges such as signal integrity and EMC, while ensuring manufacturability.

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